

**IN THE CLAIMS:**

The following is a complete listing of the claims in this application, reflects all changes currently being made to the claims, and replaces all earlier versions and all earlier listings of the claims:

1. (Currently Amended) A head substrate of a printing head detachably mounted on a printer main body, comprising:

plural external connection terminals individually receiving, from the exterior, a binary logic signal corresponding to whether or not to execute a recording operation, a recording image signal and a clock signal;

recording execution means for executing the recording operation according to the recording image signal and the clock signal entered through said external connection terminals, in case said the binary logic signal is in a first state;

data memory means for executing a memory access which is at least either of data writing and data readout; and

memory access means for recognizing said the binary logic signal as an access permission signal and executing the memory access to said data memory means at a timing when said in case the logic signal is in a second state.

2. (Currently Amended) A head substrate according to claim 1, wherein said external connection terminals include:

first common terminal wiring means for supplying said the externally entered binary logic signal to said memory access means and to said recording execution means; and

second common terminal wiring means for supplying said the externally entered clock signal as a recording clock signal to said recording execution means and as a memory clock signal to said memory access means.

3. (Previously Presented) A head substrate according to claim 2, wherein:

said recording execution means is adapted for executing the recording operation based on the recording image signal serially entered into one of said external connection terminals; and

said common terminal wiring means allows at least one of the input of data into said memory access means and the output of data from said memory access means, using the same external connection terminal that serially inputs the recording image signal.

4. (Canceled)

5. (Currently Amended) A head substrate according to claim 2, wherein:

said recording execution means is adapted for executing the recording operation based on the recording image signal parallel entered into plurality of said external connection terminals; and

said common terminal wiring means allows at least one of the parallel input of data into said memory access means and the parallel output of data from said memory access means, using the same external connection terminal that ~~parallelly~~ inputs, in parallel, the recording image signal.

6. (Canceled)

7. (Currently Amended) A head substrate according to claim 2,

wherein:

said recording execution means includes a shift register which is reset by a reset signal externally entered into one of said external connection terminals and is adapted to temporarily hold and parallel output, at a timing corresponding to the clock signal, the recording image signal serially entered into another of said external connection terminals; and

said common terminal wiring means is adapted for supplying said memory access means with the reset signal for said the shift register, as said the binary logic signal constituting said the access permission signal.

8. (Canceled)

9. (Currently Amended) A head substrate according to claim 2,

wherein:

said recording execution means includes a shift register which is adapted to be reset by a reset signal externally entered into one of said external connection terminals and then to temporarily hold and parallel output, at a timing corresponding to the clock signal, the recording image signal serially entered into another of said external connection terminals, and a latch circuit which is adapted to be reset by said the reset signal and then to temporarily hold and output the recording image signal parallel outputted from said the shift register; and

said common terminal wiring means is adapted for supplying said memory access means with said the reset signal as said the binary logic signal constituting said the access permission signal.

10. (Currently Amended) A head substrate according to claim 2, wherein:

said recording execution means includes a shift register which is adapted to be reset by a reset signal externally entered into one of said external connection terminals and then to temporarily hold and parallel output, at a timing corresponding to the clock signal, the recording image signal serially entered into another of said external connection terminals, and a latch circuit which is adapted to temporarily hold and output the recording image signal parallel outputted from said the shift register at a timing corresponding to a latch signal externally entered into still another of said external connection terminals; and

said common terminal wiring means is adapted for supplying said memory access means with said the latch signal as said the binary logic signal constituting said the access permission signal.

11. (Currently Amended) A head substrate according to claim 1, wherein said recording execution means includes plural recording elements for recording the recording image signal parallel outputted from said the latch circuit, corresponding to a recording pulse signal externally entered into one of said external connection terminals.

12. (Currently Amended) A head substrate according to claim 11, wherein said the recording element is a heat generating element.

13. (Currently Amended) A head substrate according to claim 2, wherein said common terminal wiring means is adapted to supply said memory access means with the clock signal for ~~said~~ the recording image signal, as a memory clock signal.

14. (Currently Amended) A head substrate according to claim 2, wherein:

said data memory means ~~is means for executing~~ executes both data writing and data readout as the memory access;

said memory access means ~~is means for selectively executing~~ executes either of data writing into and data readout from said data memory means corresponding to an externally entered mode switching signal; and

said common terminal wiring means ~~is adapted for supplying~~ supplies said memory access means with the input signal to one of said external connection terminals as the mode switching signal.

15. (Previously Presented) A head substrate according to claim 2, wherein:

said recording execution means is adapted for receiving a driving electric power externally entered from one of said external connection terminals; and

said common terminal wiring means is adapted for supplying said memory access means with the driving electric power for said recording execution means.

16. (Previously Presented) A head substrate according to claim 2, wherein said external connection terminals, said recording execution means, said data

memory means, said memory access means and said common terminal wiring means are constituted by films formed on one base substrate.

17. (Previously Presented) A printing head detachably mounted on a printer main body, comprising a head substrate according to claim 1.

18. and 19. (Canceled)

20. (Previously Presented) A printing head according to claim 17, wherein the execution means includes heat recording elements for discharging ink.

21. (Currently Amended) A printing head detachably mounted on a printer main body, comprising:

plural external connection terminals individually receiving, from the exterior, a binary logic signal corresponding to whether or not to execute a recording operation, a recording image signal and a clock signal;

recording execution means for executing the recording operation according to the recording image signal and the clock signal entered through said external connection terminals, in case ~~said~~ the binary logic signal is in a first state;

data memory means for executing a memory access which is at least either of data writing and data readout; and

memory access means for recognizing ~~said~~ the binary logical signal as an access permission signal and executing the memory access to said data memory means ~~at a timing when said~~ in case the logical signal is in a second state.

22. (Currently Amended) A printing head according to claim 21, wherein said external connection terminals include:

first common terminal wiring means for supplying ~~said~~ the externally entered binary logic signal to said memory access means and to said recording execution means; and

second common terminal wiring means for supplying ~~said~~ the externally entered clock signal as a recording clock signal to said recording execution means and as a memory clock signal to said memory access means.

23. (Currently Amended) A printing apparatus comprising:

a printing head according to claim 17;

input means for individually transmitting the binary logic signal of the first state and various signals such as the recording image signal and the clock signal respectively to plurality of said external connection terminals of said printing head, ~~thereby~~ causing said recording execution means to execute a recording operation; and

access control means for transmitting the binary logic signal of the second state and the clock signal[[, etc.]] to said plural external connection terminals of said printing head, ~~thereby~~ causing said memory access means to execute the memory access.

24. (Currently Amended) A printing apparatus comprising:

a printing head according to claim 21;

input means for individually transmitting the binary logic signal of the first state and various signals such as the recording image signal and the clock signal respectively to said plurality of external connection terminals of said printing head, thereby causing said recording execution means to execute a recording operation; and

access control means for transmitting the binary logic signal of the second state and the clock signal[, etc.]] to said plural external connection terminals of said printing head, thereby causing said memory access means to execute the memory access.

25. (Original) A printing apparatus according to claim 23, wherein:

said input means is adapted for serial transmission of the recording image signal to a specified one of said external connection terminals; and

said access control means is adapted for serial transmission of the writing data for said memory access means to one of said external connection terminals in which the recording image signal is serially entered.

26. (Original) A printing apparatus according to claim 23, wherein:

said input means is adapted for parallel transmission of the recording image signal to a specified plurality of said external connection terminals; and

said access control means is adapted for parallel transmission of the writing data for said memory access means to said plurality of external connection terminals in which the recording image signal is parallel entered.

27. (Canceled)



28. (Currently Amended) A printing apparatus according to claim 23, wherein the recording is executed by discharging ink by the heat of ~~said~~ a heat generating element.

29. (Original) A printing apparatus according to claim 24, wherein said recording execution means includes a heat generating element for recording.

30. (Currently Amended) A printing apparatus according to claim 29, wherein the recording is executed by discharging ink by the heat of ~~said~~ the heat generating element.

31.-67. (Canceled)

68. (New) A head substrate of a recording head, comprising:  
plural external connection terminals individually receiving, from the exterior, a binary logic signal corresponding to whether or not to execute a recording operation, a recording image signal and a clock signal;

recording execution means for executing the recording operation according to the recording image signal and the clock signal entered through said external connection terminals, in case the binary logic signal is in a first state;

data memory means for executing a memory access which is at least either of data writing and data readout; and

memory access means for permitting the memory access to said data  
memory means in case the logic signal is in a second state.

69. (New) A head substrate according to claim 68, wherein the logic signal is a latch signal for latching the recording image signal.

70. (New) A recording head comprising a head substrate according to claim 68.

71. (New) A recording head according to claim 70, wherein the logic signal is a latch signal for latching the recording image signal.